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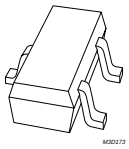


PMR780SN

N-channel µTrenchMOS™ standard level FET

Rev. 01 — 5 March 2004 **Product data**

1. Product profile

1.1 Description

N-channel enhancement mode field-effect transistor in a plastic package using TrenchMOS™ technology.

1.2 Features

- Surface mounted package
- Low on-state resistance
- Footprint 63% smaller than SOT23
- Fast switching.

1.3 Applications

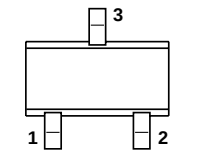
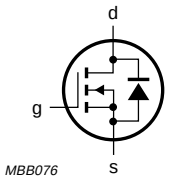
- Driver circuits
- Switching in portable appliances.

1.4 Quick reference data

- $V_{DS} \leq 60$ V
- $P_{tot} \leq 0.53$ W
- $I_D \leq 0.55$ A
- $R_{DSon} \leq 920$ mΩ.

2. Pinning information

Table 1: Pinning - SOT416 (SC-75), simplified outline and symbol

Pin	Description	Simplified outline	Symbol
1	gate (g)	 SOT416 (SC-75)	
2	source (s)		
3	drain (d)		

3. Ordering information

Table 2: Ordering information

Type number	Package		
	Name	Description	Version
PMR780SN	SC-75	Plastic surface mounted package; 3 leads	SOT416

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4. Limiting values

Table 3: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage (DC)	25 °C ≤ T_j ≤ 150 °C	-	60	V
V_{DGR}	drain-gate voltage (DC)	25 °C ≤ T_j ≤ 150 °C; R_{GS} = 20 kΩ	-	60	V
V_{GS}	gate-source voltage (DC)		-	±20	V
I_D	drain current (DC)	T_{sp} = 25 °C; V_{GS} = 10 V; Figure 2 and 3	-	0.55	A
		T_{sp} = 100 °C; V_{GS} = 10 V; Figure 2	-	0.35	A
I_{DM}	peak drain current	T_{sp} = 25 °C; pulsed; t_p ≤ 10 μs; Figure 3	-	1.1	A
P_{tot}	total power dissipation	T_{sp} = 25 °C; Figure 1	-	0.53	W
T_{stg}	storage temperature		–55	+150	°C
T_j	junction temperature		–55	+150	°C
Source-drain diode					
I_S	source (diode forward) current (DC)	T_{sp} = 25 °C	-	0.44	A
I_{SM}	peak source (diode forward) current	T_{sp} = 25 °C; pulsed; t_p ≤ 10 μs	-	0.88	A

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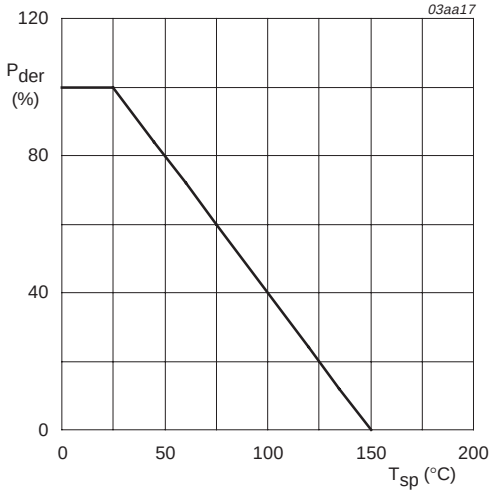
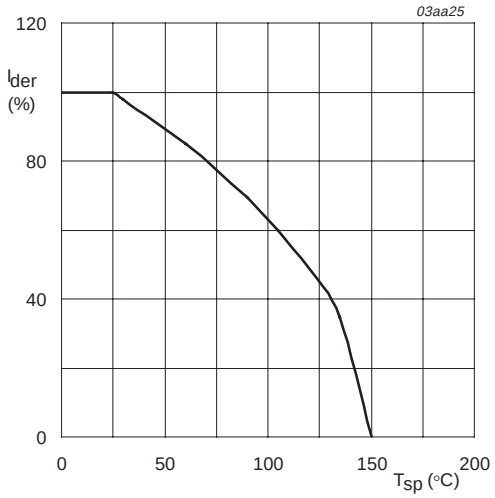


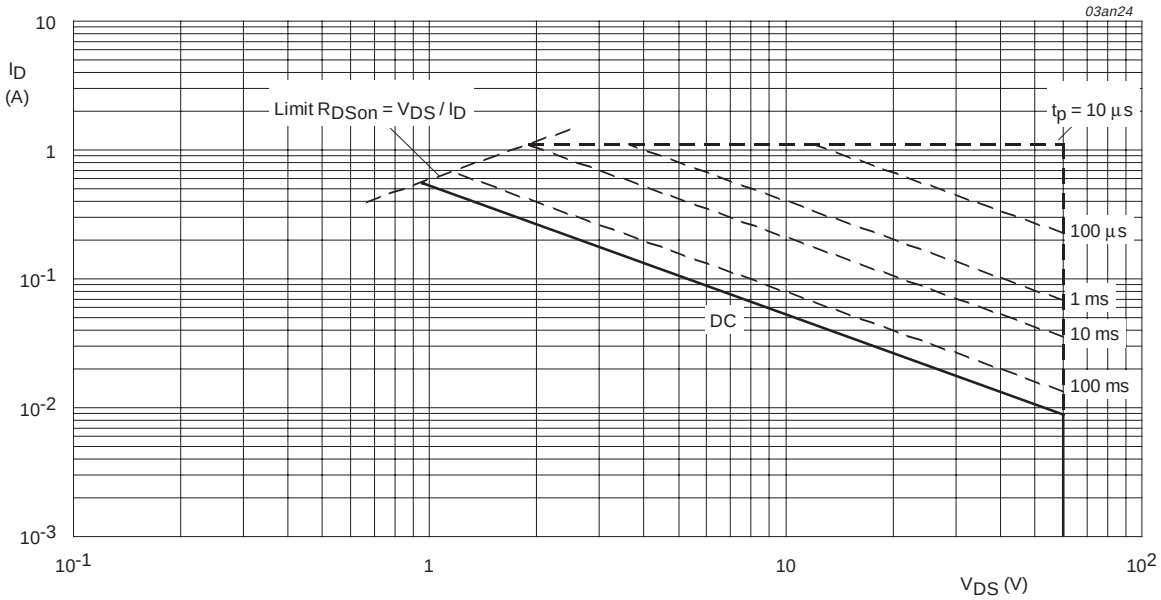
$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ C)}} \times 100\%$$

Fig 1. Normalized total power dissipation as a function of solder point temperature.



$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100\%$$

Fig 2. Normalized continuous drain current as a function of solder point temperature.



T_{sp} = 25 °C; I_{DM} is single pulse; V_{GS} = 10 V.

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage.

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5. Thermal characteristics

Table 4: Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	Figure 4	-	-	235	K/W

5.1 Transient thermal impedance

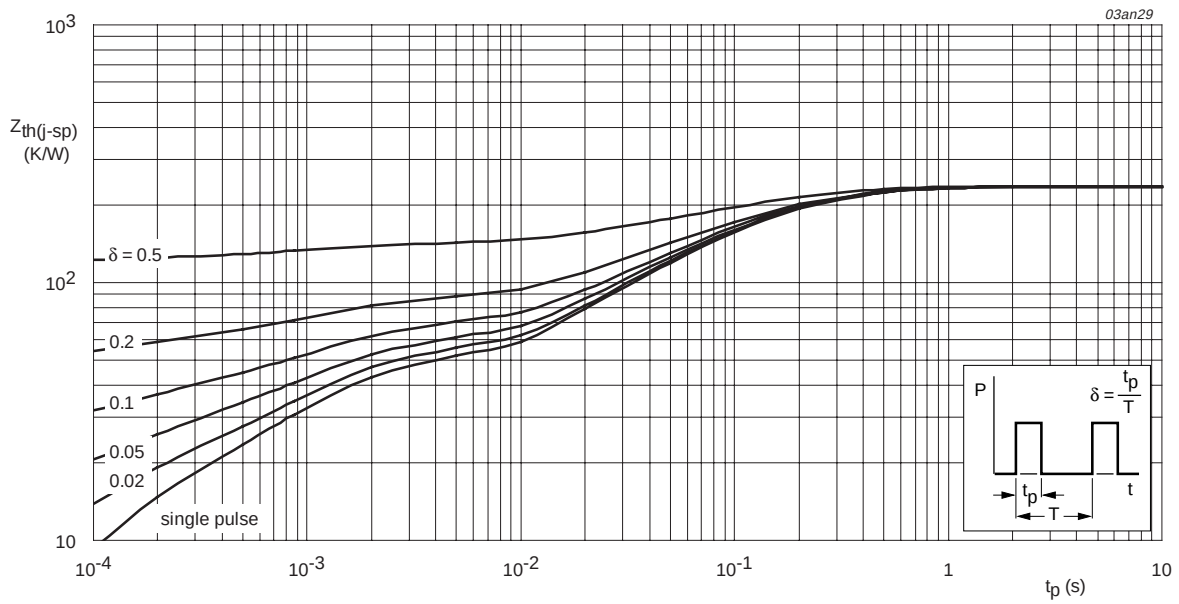

Fig 4. Transient thermal impedance from junction to solder point as a function of pulse duration.

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6. Characteristics

Table 5: Characteristics

T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 1 μA; V_{GS} = 0 V				
		T_j = 25 °C	60	-	-	V
		T_j = –55 °C	55	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 0.25 mA; V_{DS} = V_{GS}; Figure 9				
		T_j = 25 °C	1	2	3	V
		T_j = 150 °C	0.5	-	-	V
		T_j = –55 °C	-	-	3.4	V
I_{DSS}	drain-source leakage current	V_{DS} = 60 V; V_{GS} = 0 V				
		T_j = 25 °C	-	0.05	1	μA
		T_j = 100 °C	-	-	100	μA
I_{GSS}	gate-source leakage current	V_{GS} = ±20 V; V_{DS} = 0 V	-	10	100	nA
R_{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_D = 0.3 A; Figure 7 and 8				
		T_j = 25 °C	-	780	920	mΩ
		T_j = 150 °C	-	1445	1700	mΩ
		V_{GS} = 4.5 V; I_D = 0.075 A; Figure 7 and 8	-	1100	1400	mΩ
Dynamic characteristics						
$Q_{g(tot)}$	total gate charge	I_D = 1 A; V_{DD} = 30 V; V_{GS} = 10 V; Figure 13	-	1.05	-	nC
Q_{gs}	gate-source charge		-	0.2	-	nC
Q_{gd}	gate-drain (Miller) charge		-	0.22	-	nC
C_{iss}	input capacitance	V_{GS} = 0 V; V_{DS} = 30 V; f = 1 MHz; Figure 11	-	23	-	pF
C_{oss}	output capacitance		-	5	-	pF
C_{rss}	reverse transfer capacitance		-	3.5	-	pF
$t_{d(on)}$	turn-on delay time	V_{DD} = 30 V; R_L = 15 Ω; V_{GS} = 10 V; R_G = 6 Ω	-	2	-	ns
t_r	rise time		-	4	-	ns
$t_{d(off)}$	turn-off delay time		-	5	-	ns
t_f	fall time		-	2.2	-	ns
Source-drain diode						
V_{SD}	source-drain (diode forward) voltage	I_S = 0.3 A; V_{GS} = 0 V; Figure 12	-	0.83	1.2	V

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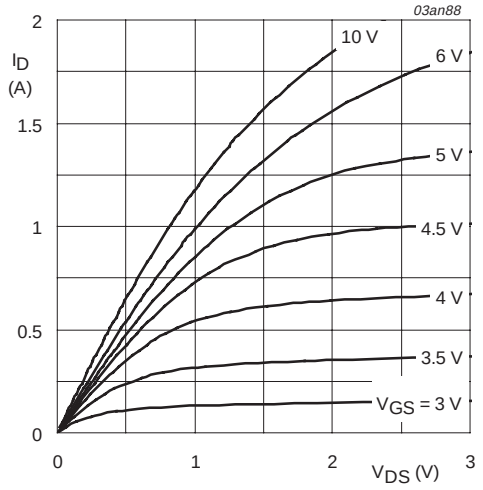
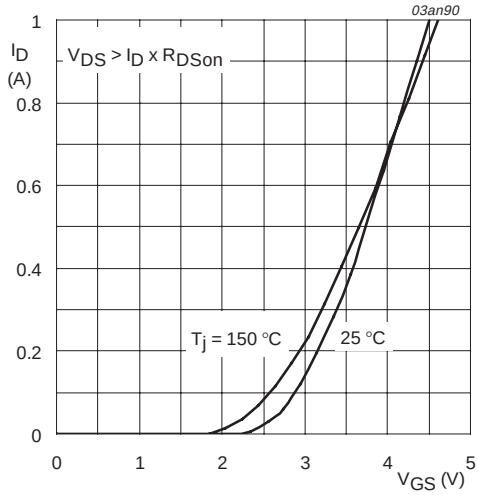


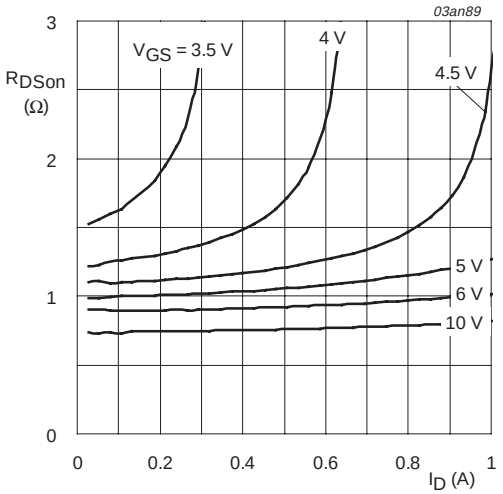
T_j = 25 °C

Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values.



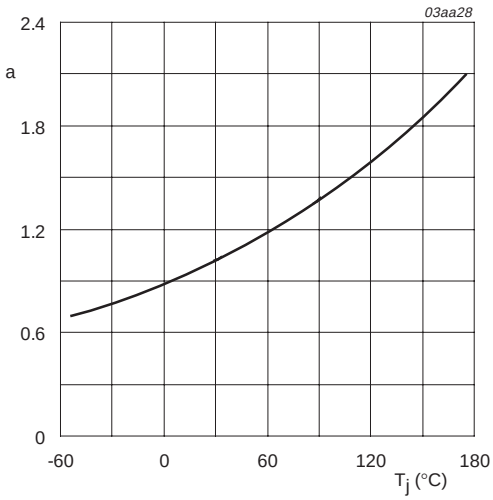
T_j = 25 °C and 150 °C; $V_{DS} > I_D \times R_{DSon}$

Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values.



T_j = 25 °C

Fig 7. Drain-source on-state resistance as a function of drain current; typical values.



$$a = \frac{R_{DSon}}{R_{DSon(25^\circ C)}}$$

Fig 8. Normalized drain-source on-state resistance factor as a function of junction temperature.

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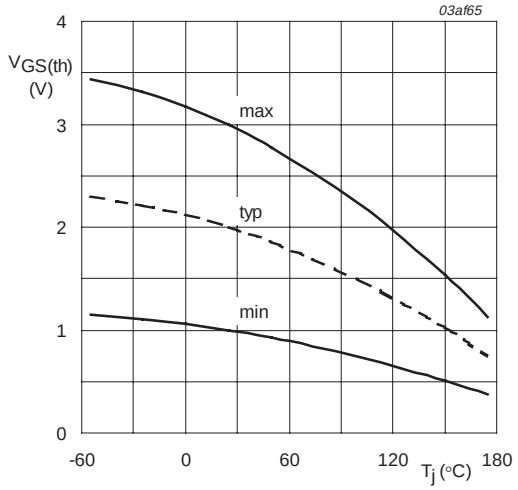
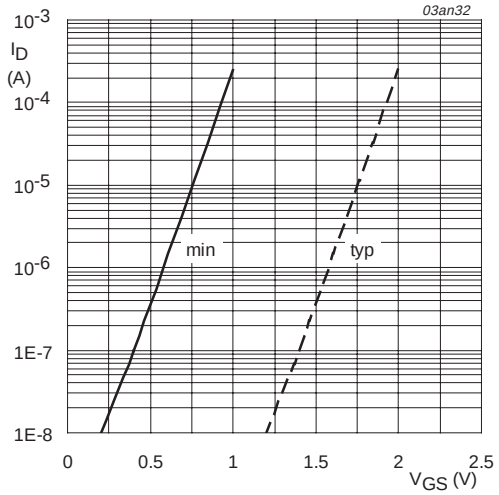


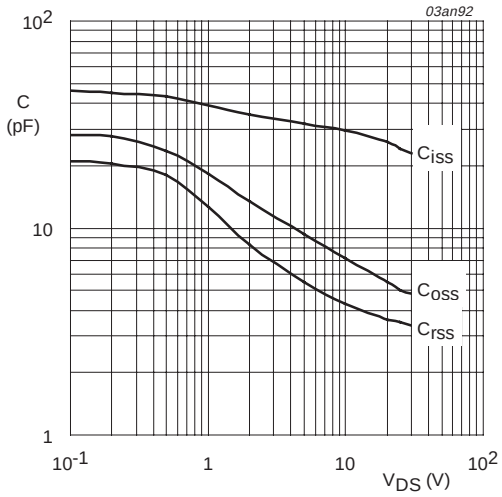
I_D = 0.25 mA; V_{DS} = V_{GS}

Fig 9. Gate-source threshold voltage as a function of junction temperature.



T_j = 25 °C; V_{DS} = 5 V

Fig 10. Sub-threshold drain current as a function of gate-source voltage.



V_{GS} = 0 V; f = 1 MHz

Fig 11. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values.

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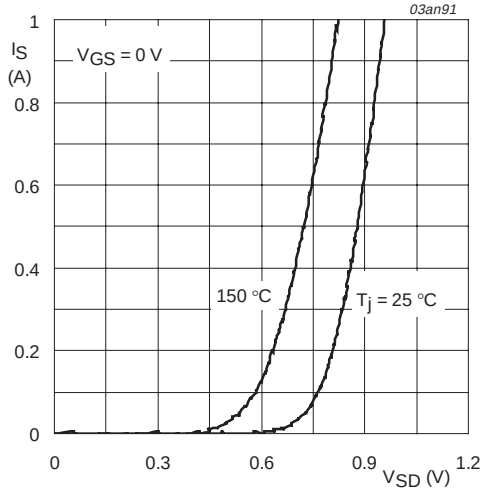
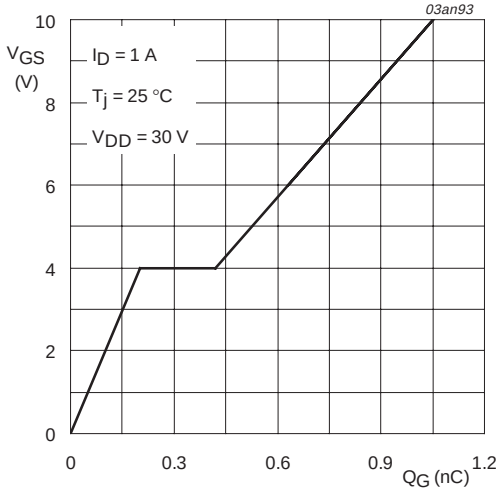


T_j = 25 °C and 150 °C; V_{GS} = 0 V

Fig 12. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values.



I_D = 1 A; V_{DD} = 30 V

Fig 13. Gate-source voltage as a function of gate charge; typical values.

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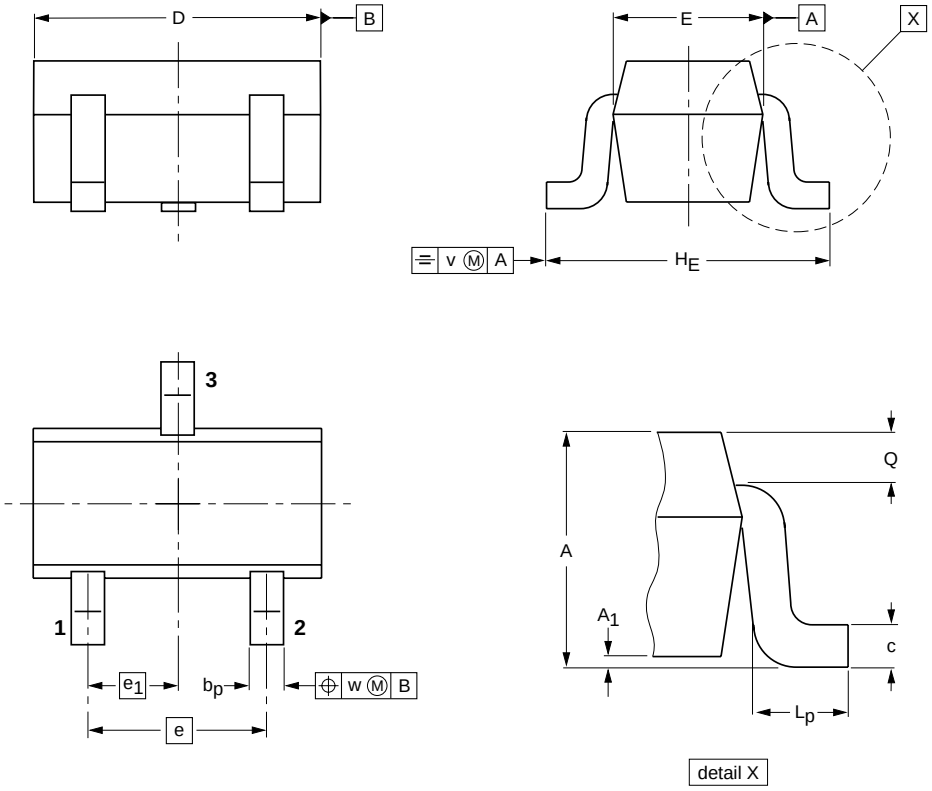
7. Package outline

Plastic surface mounted package; 3 leads **SOT416**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w
mm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT416			SC-75			97-02-28

Fig 14. SOT416 (SC-75).

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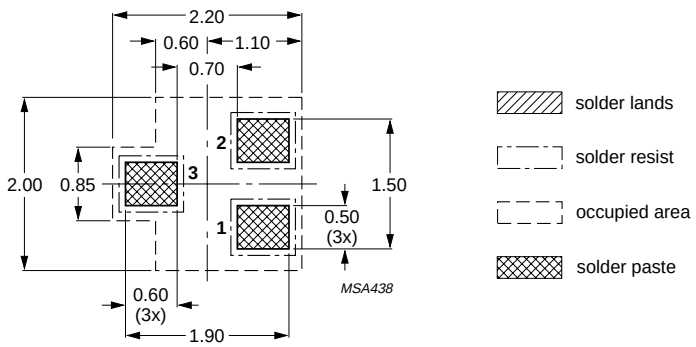
8. Soldering



Dimensions in mm.

Fig 15. Reflow soldering footprint for SOT416 (SC-75).

9. Revision history

Table 6: Revision history

Rev	Date	CPCN	Description
01	20040305	-	Product data (9397 750 12666)

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10. Data sheet status

Level	Data sheet status[1]	Product status[2][3]	Definition
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Contents

1 Product profile 1
1.1 Description 1
1.2 Features 1
1.3 Applications 1
1.4 Quick reference data. 1
2 Pinning information 1
3 Ordering information 1
4 Limiting values 2
5 Thermal characteristics. 4
5.1 Transient thermal impedance 4
6 Characteristics 5
7 Package outline 9
8 Soldering 10
9 Revision history 10
10 Data sheet status 11
11 Definitions 11
12 Disclaimers 11
13 Trademarks 11

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Date of release: 5 March 2004 **Document order number: 9397 750 12666**

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